

**RECORDATION FORM COVER SHEET  
PATENTS ONLY**

Docket No. XA-10573

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

**1. Name of conveying party(ies)/Execution Date(s):**

HANZAWA, Satoru  
SHIGETA, Junji  
KIMURA, Shinichiro  
SAKATA, Takeshi  
TAKEMURA, Riichiro  
KAJIGAYA, Kazuhiko

Execution Date(s): 5/15/2006; 5/23/2006; 5/17/2006;  
5/15/2006; 5/15/2006; 5/16/2006

Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

**3. Nature of conveyance:**

- ☒ Assignment ☐ Merger  
☐ Security Agreement ☐ Change of Name  
☐ Government Interest Assignment  
☐ Executive Order 9424, Confirmatory License  
☐ Other \_\_\_\_\_

**2. Name and address of receiving party(ies)**

Name: Hitachi, Ltd.

Address: 6-6, Marunouchi 1-chome

City: Chiyoda-ku

State: Tokyo

Country: Japan Zip: 100-8280

Additional name(s) & address(es) attached? ☒ Yes ☐ No

**4. Application or patent number(s):**

A. Patent Application No.(s)

PCT/JP03/14901 filed 11/21/2003  
(now U.S. Appln. No. 10/579,911)

☐ This document is being filed together with a new application.  
B. Patent No.(s)

Additional numbers attached? ☐ Yes ☒ No

**5. Name and address to whom correspondence concerning document should be mailed:**

Name: Mitchell W. Shapiro

Registration No.: 31,568

Address: Miles & Stockbridge P.C.

1751 Pinnacle Drive

Suite 500

City: McLean

State: Virginia Zip: 22102

Phone Number: 703-610-8652

Fax Number: 703/610-8686

Email Address: mws Shapiro@milestockbridge.com

**6. Total number of applications and patents involved: 1**

**7. Total fee (37 CFR 1.21(h) & 3.41) \$40.00**

- ☐ Authorized to be charged to deposit account  
☒ Enclosed – Any excess or insufficiency should be credited or debited to deposit account

**8. Payment Information**

a. Credit Card Last 4 Numbers \_\_\_\_\_

Expiration Date \_\_\_\_\_

b. Deposit Account Number 50-1165

Authorized User Name Mitchell W. Shapiro

**9. Signature:**

Mitchell W. Shapiro

Name of Person Signing

Date April 9, 2007

Total Number of pages including cover sheet, attachments, and documents: 4

04/11/2007 LLANDGRA 00000007 10579911

02 FC:8921

40.00 DP

**RECORDATION FORM COVER SHEET  
(FORM PTO-1595) CONTINUATION SHEET**

Continuation of Box 2:

**Receiving Party**

Name (line 1) | \_\_\_\_\_ Elpida Memory, Inc. \_\_\_\_\_ |  
Name (line 2) | \_\_\_\_\_ |  
Address (line 1) | \_\_\_\_\_ 2-1, Yaesu 2-chome \_\_\_\_\_ |  
Address (line 2) | \_\_\_\_\_ Chuo-ku, Tokyo, 104-0028 \_\_\_\_\_ |  
Address (line 3) | \_\_\_\_\_ Japan \_\_\_\_\_ |

**Receiving Party**

Name (line 1) | \_\_\_\_\_ |  
Name (line 2) | \_\_\_\_\_ |  
Address (line 1) | \_\_\_\_\_ |  
Address (line 2) | \_\_\_\_\_ |  
Address (line 3) | \_\_\_\_\_ |

**Receiving Party**

Name (line 1) | \_\_\_\_\_ |  
Name (line 2) | \_\_\_\_\_ |  
Address (line 1) | \_\_\_\_\_ |  
Address (line 2) | \_\_\_\_\_ |  
Address (line 3) | \_\_\_\_\_ |

# ASSIGNMENT

( 譲渡証 )

As a below named inventor, I hereby declare that:

IN CONSIDERATION of the sum of One Dollar (\$1.00) or the equivalent thereof, and other good and valuable consideration paid to Satoru HANZAWA, Junji SHIGETA, Shinichiro KIMURA, Takeshi SAKATA, Riichiro TAKEMURA and Kazuhiko KAJIGAYA,  
Citizen(s) of Japan by HITACHI, LTD., and Elpida Memory, Inc., corporation(s) organized under the laws of Japan,

located at 6-6, Marunouchi 1-chome; Chiyoda-ku, Tokyo 100-8280 Japan, and 2-1, Yaesu 2-chome, Chuo-ku, Tokyo 104-0028 Japan, respectively,

receipt of which is hereby acknowledged, we/I, the said Satoru Hanzawa, Junji SHIGETA, Shinichiro KIMURA, Takeshi SAKATA, Riichiro TAKEMURA and Kazuhiko KAJIGAYA,

do hereby sell and assign to said HITACHI, LTD., and Elpida Memory, Inc.,

their successors and assigns, all our right, title and interest, in and for the United States of America, in and to

## SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

invented by us/me and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., and Elpida Memory, Inc.,

their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by us if this assignment and sale had not been made;

And we/I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of said above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And we/I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD., and Elpida Memory, Inc.

Signed on the date(s) indicated aside signatures:

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1) <u>Satoru Hanzawa</u> (Satoru HANZAWA)	<u>5/15/2006</u>
2) <u>Junji Shigeta</u> (Junji SHIGETA)	<u>5/23/2006</u>
3) <u>Shin'ichiro Kimura</u> (Shinichiro KIMURA)	<u>5/17/2006</u>
4) <u>Takeshi Sakata</u> (Takeshi SAKATA)	<u>5/15/2006</u>
5) <u>Riichiro Takemura</u> (Riichiro TAKEMURA)	<u>5/15/2006</u>
6) _____ (Kazuhiko KAJIGAYA)	_____
7) _____	_____

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do hereby sell and assign to said HITACHI, LTD., and Elpida Memory, Inc.,  
their successors and assigns, all our right, title and interest, in and for the United States of America, in and to

## SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

invented by us/me and described in the application for United States Letters Patent therefor, executed on even date herewith, and all United States Letters Patent which may be granted therefor, and all divisions, continuations and extensions thereof, the said interest being the entire ownership of the said Letters Patent when granted, to be held and enjoyed by said HITACHI, LTD., and Elpida Memory, Inc.,  
their successors, assigns or other legal representatives, to the full end of term for which said Letters Patent may be granted, as fully and entirely as the same would have been held and enjoyed by us if this assignment and sale had not been made;

And we/I hereby agree to sign and execute any further documents or instruments which may be necessary, lawful, and proper in the prosecution of said above-named application or in the preparation and prosecution of any continuing, continuation-in-part, substitute, divisional, renewal, reviewed or reissue applications or in any amendment, extension, or interference proceedings, or otherwise to secure the title thereto in said assignee;

And we/I do hereby authorize and request the Commissioner of Patents to issue said Letters Patent to said HITACHI, LTD., and Elpida Memory, Inc.

Signed on the date(s) indicated aside signatures:

INVENTOR(S) (発明者フルネームサイン)	Date Signed (署名日)
1) _____ (Satoru HANZAWA)	_____
2) _____ (Junji SHIGETA)	_____
3) _____ (Shinichiro KIMURA)	_____
4) _____ (Takeshi SAKATA)	_____
5) _____ (Riichiro TAKEMURA)	_____
6) <u>Kazuhiko Kajigaya</u> (Kazuhiko KAJIGAYA)	<u>16 / May / 06</u>
7) _____	_____